

surfaces 141, 143 and 145, respectively, which contact the bottom surfaces of circuit boards 11, 13 and 15 at locations between the IC chips 12 on the bottom of the board. Exemplary upper surfaces 141, 143 and 145 comprise curved upper surfaces of ribs (blocks) 140, 142 and 144, respectively. Preferably, such upper surfaces are provided at a height approximately equal to a thickness of integrated circuit chip components 12. Accordingly, when boards 11, 13 and 15 are rested on panels 132, 134 and 136, respectively, the boards rest on the upper surfaces of blocks 140, 142 and 144 while leaving integrated circuit chip components on the underside of boards 11, 13 and 15 extending between block upper surfaces 141, 143 and 145 and panels 132, 134 and 136. An exemplary block height (or thickness) of blocks 140, 142 and 144 for a DRAM having IC chips 12 and TSOP dimensional package is 0.040 inches ± 0.005 inches. As another example, if IC chips 12 have a S0J dimensional package, the block height is preferably 0.140 inches ± 0.005 inches.

In the Claims

Please replace the claims with the following clean version of the entire set of pending claims, in accordance with 37 C.F.R. § 1.121(c)(1)(i). Cancel all previous versions of any pending claim.

A marked up version showing amendments to any claims being changed is provided in one or more accompanying pages separate from this amendment in accordance with 37 C.F.R. § 1.121(c)(1)(ii). Any claim not accompanied by